

JTAG-C1

April 24

Overview

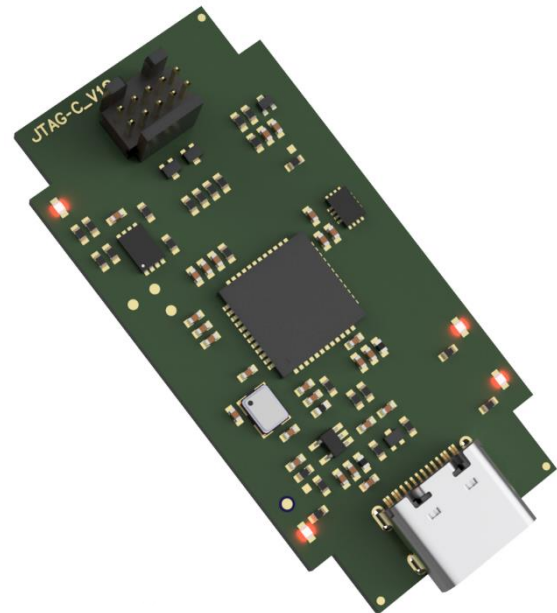
The JTAG-C1 adapter is an affordable high-speed Lattice FPGA programming solution. It is based on FTDI USB to UART/FIFO IC (FT4232HQ) enabling JTAG communication of up to 30Mbits/s.

The adapter connects a USB Type-C port to a 10-pin 1.27mm pitch header (Samtec FTSH-105-01-L-D-K). This pin header allows the user to connect to the JTAG programming interface of the target board through a flat cable. A wide range of JTAG voltages (0.8V – 3.6V) is supported.

Support for Lattice Radiant Programmer Software is planned in the future.

All Features

- Tiny 22mm x 45mm programming probe for Lattice Nexus FPGAs
- Self-powered through a USB-C connector
- Vref to support different JTAG-Target voltages; can be between 0.8V and 3.6V
- USB 2.0 high-speed interface
- JTAG communication support up to 30Mbits/s
- Board Connectors:
 - USB Type-C
 - FTSH-105-01-L-D-K 1.27mm pitch, 10pins
- Compatible flat cables:
 - FFSD-05-D
 - FFTP-05-D
 - Adafruit 1675 (150mm)



2	TMS	VCC	1
4	TCK	NC	3
6	TDO	NC	5
8	TDI	NC	7
×10	RST	GND	9

FTSH-105-01-L-D-K

Ordering Information

Description	Ordering Part Number
JTAG-C1 + flat cable	JTAG-C1
JTAG-C1 + flat cable + plastic housing	JTAG-C1-H